

4 a transmitter for transmitting signals on a
5 single communications channel;
6 a receiver for receiving signals on the single
7 communications channel;
8 processing means for indicating whether the UART
9 is operating in a full duplex mode of operation or whether
10 a receiver enable flag has been set; and
11 receiver control circuitry for disabling [and
12 enabling] the receiver in response to at least one of the
13 indication of a full duplex mode of operation and the
14 indication of the setting of the receiver enable flag by
15 the processing means.

REMARKS

In response to the Office Action dated October 22, 1998, the Applicant has canceled Claim 16 and amended Claim 11. Thus, Claims 11-15 and 17-21 remain pending in the Application. Reconsideration of the claims, as amended, is respectfully requested.

The Examiner rejected Claims 11-13 under 35 U.S.C. §102(b) as being anticipated by Klinger et al. Applicant has amended Claim 11 to include the limitations of previous Claim 16. In view of the indication of allowable subject matter for Claim 16, the Applicant respectfully submits that Claim 11 is now in condition for allowance. Thus, the Notice of Allowance for